

GOVERNMENT POLYTECHNIC COLLEGE CHITTORGARH

Time Table III and V Semester (Electronics engineering) Session 2023-24 w.e.f. 07/08/2022

DAY	Semester	I	II	III	Recess	IV	V	VI
		9.30 - 10.30 -	10.30 - 11.30 -	11.30 - 12.30 -		1.00- 2.00-	2.00- 3.00-	3.00- 4.00-
Mon	III	EL3002L JKJ	EL3001L MJ	EL3003L JKJ		EL3009 PKT(P)		
	V	EL5001L MJ	EL50032L JKJ	EL5002L PKT		EL5005 MJ(P)		
Tue	III	EL3003L JKJ	EL3004L PKT	EL3002L JKJ		EL3006 MJ(P)		
	V	EL5001L MJ	EL50041L JKJ	EL5002L PKT		EL50072 JKJ(P)		
Wed	III	EL3001L MJ	EL3002L JKJ	EL3005L MJ		EL3008 JKJ(P)		
	V	EL51001L PKT	EL5001L MJ	EL50032L JKJ		EL5006 PKT(P)		
Thu	III	EL3001L MJ	EL3003L JKJ	EL3005L MJ		EL3007 JKJ(P)		
	V	EL50041L JKJ	EL5002L PKT	EL50032L JKJ		EL5010 MJ(P)		
Fri	III	EL3001L MJ	EL3004L PKT	EL3005 MJ(T)		SCA PKT		
	V	EL51001L PKT	EL5001L MJ	EL50041L JKJ		EL50081 JKJ(P)		EL5555 MJ
Sat	III	EL3333 MJ	EL3004L PKT					
	V	EL51001L PKT				SCA PKT		

Note: Discrepancy (If any) to be communicated to the undersigned.

OC/TT

Shabir Khan

[Signature]
PRINCIPAL